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	Application No.	Applicant(s)	
N. 4: of Allowahility	10/840,030	RIBARICH ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Thuy V. Tran	2821	
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication IGHTS. This application is subject to	plication. If not included will be mailed in due c	d ourse. THIS
1. \boxtimes This communication is responsive to <u>amendment submitted</u>	<u>d on 4/13/2006</u> .		
2. The allowed claim(s) is/are <u>2-5 and 7-25</u> .			
Acknowledgment is made of a claim for foreign priority un a)	e been received.		
2. Certified copies of the priority documents have	• • • • • • • • • • • • • • • • • • • •		am fram tha
 Copies of the certified copies of the priority documents International Bureau (PCT Rule 17.2(a)). 	cuments have been received in this	national stage application	on from the
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submit	IENT of this application. itted. Note the attached EXAMINER	'S AMENDMENT or NO	
INFORMAL PATENT APPLICATION (PTO-152) which give	•	ition is deficient.	
5. CORRECTED DRAWINGS (as "replacement sheets") mus		040) allachad	
 (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date 	•	946) attached	
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date		Office action of	
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the			back) of
 DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT I 			ote the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. ☐ Notice of Informal P	atent Application (PTO	152\
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary	,	-102)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Dat	te	
Paper No./Mail Date			
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 	8. Examiner's Stateme 9. Other TH	UY WTRAN RY EXAMINER	vance

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DETAILED ACTION

This is a response to the Applicants' amendment submitted on April 13, 2006. In virtue of this amendment, claims 1 and 6 are canceled; and thus, claims 2-5 and 7-25 are now presented in the instant application.

Allowable Subject Matter

1. Claims 2-5 and 7-25 are allowed.

Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance:

Prior art fails to disclose or fairly suggest:

- An electronic ballast control for controlling a power switch in an electronic ballast to switch power to a load wherein the control is implemented as a single integrated circuit and further comprising a minimum frequency signal applied to the oscillator to determine a minimum oscillation frequency provided by the oscillator, in combination with the remaining claimed limitations as called for in independent claim 2 (claims 3-5, 7-8, and 10 are allowed since they are dependent on claim 2);
- An electronic ballast control for controlling a power switch in an electronic ballast to switch power to a load wherein the oscillator is a digital oscillator, in combination with the remaining claimed limitations as called for in independent claim 9;
- An electronic ballast control for controlling a power switch in an electronic ballast to switch power to a load further comprising a DAC in the oscillator for converting an input digital signal to an analog signal, whereby the oscillation frequency is related to the analog signal, in combination with the remaining claimed limitations as called for

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in independent claim 11;

- A method of operating an electronic ballast comprising applying the parameters to ballast control components to obtain selected operating points for the components, including applying a digital oscillator control signal to a digital oscillator in the control, and D/A converting said oscillator control signal to an analog signal, whereby the oscillation frequency is related to the analog signal, in combination with the remaining claimed limitations as called for in independent claim 12 (claims 13-17 are allowed since they are dependent on claim 12);
- A method of operating an electronic ballast further comprising counting a number of
 events in the ballast control to determine when the number of events reach a
 predetermined value in a specified time period, in combination with the remaining
 claimed limitations as called for in independent claim 18;
- A method of operating an electronic ballast further comprising timing one or more
 events to determine if a predetermined time duration is achieved for the one or more
 events, in combination with the remaining claimed limitations as called for in
 independent claim 19;
- A ballast control IC comprising a digital oscillator coupled to the controller for
 receiving a digital oscillation set point and providing an oscillation signal based on
 the set point, in combination with the remaining claimed limitations as called for in
 independent claim 20 (claims 21-22 are allowed since they are dependent on claim
 20);
- An electronic ballast control for controlling a power switch in an electronic ballast to

switch power to a load further comprising an input section of said integrated circuit for inputting said parameters, said input section comprising only two pins of said integrated circuit for receiving a data signal and a clock signal, in combination with the remaining claimed limitations as called for in independent claim 23 (claim 24 is allowed since it is dependent on claim 23); and

• An electronic ballast control for controlling a power switch in an electronic ballast to switch power to a load wherein said integrated circuit has a pair of shutdown pins for inputting control parameters corresponding to a pair of loads, said control parameters enabling said control to shut down if one load is removed, and/or to continue running if one load is removed, and/or to initiate a start-up operation when a removed load is replaced by a new load, in combination with the remaining claimed limitations as called for in independent claim 25.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuy V. Tran whose telephone number is (571) 272-1828. The examiner can normally be reached on M-F (8:00 AM -4:00 PM).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

04/30/2006

THUY V. TRAN
PRIMARY EXAMINER



Application No.: 10/840,030 Amendment dated October 26, 2005 Reply to Office Action of July 26, 2005 REPLACEMENT SHEET

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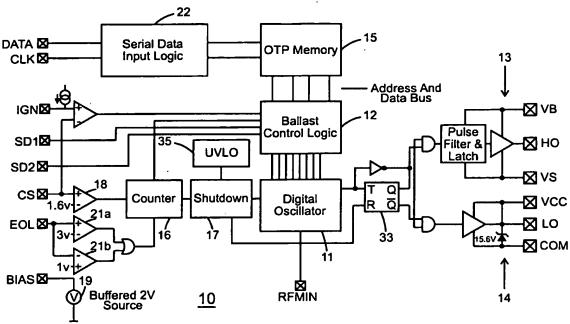
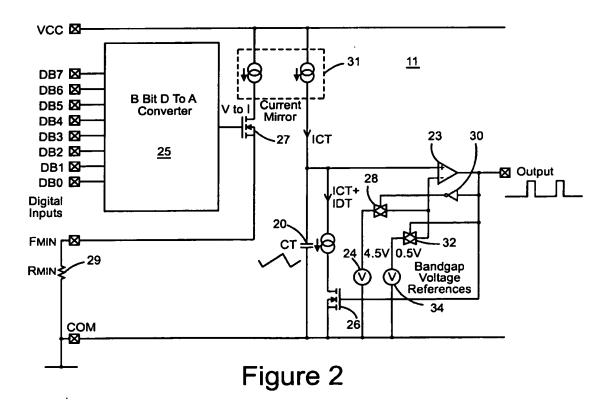


Figure 1



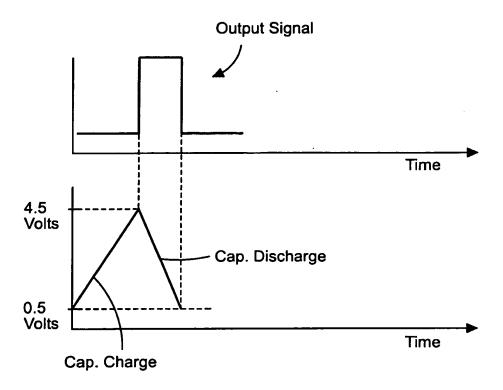


Figure 3

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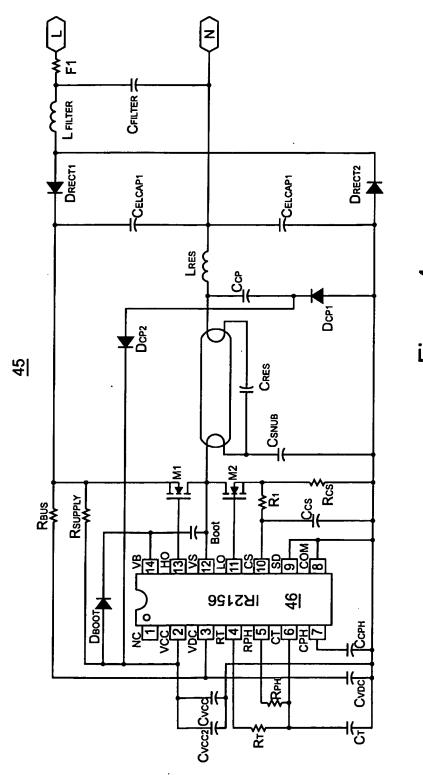


Figure 4

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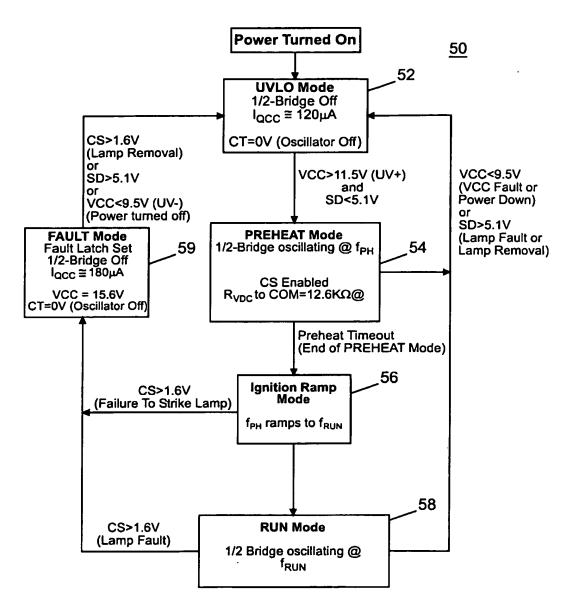


Figure 5